

1. General Description

WP1268 can disconnect the systems from its output pin (OUT) in case wrong input operating conditions are detected.

The internal overvoltage thresholds (OVLO) is 1.2V. WP1268 also has internal over temperature protect (OTP) function and it can monitor chip temperature to protect the device.

The device is available in SOT23-5 Green package.

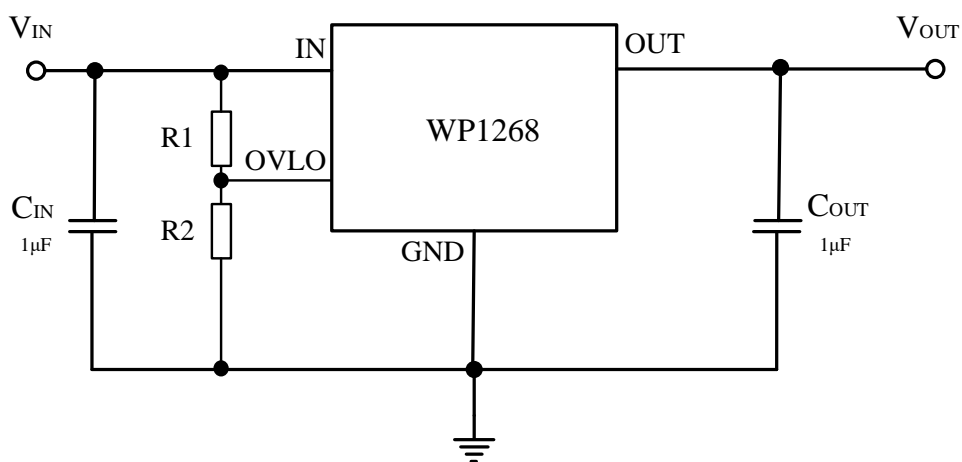
2. Features

- Typical $R_{DS(ON)}$: 110mΩ N-Channel MOSFET@12V/1A
- V_{IN} Operating Range: 4V to 60V
- Adjustable OVLO Threshold Range: 15V~24V
- OVP Response Time: 300ns (Typ.)
- Startup Debounce Time: 12ms (Typ.)
- Internal Thermal-Shutdown Protection
- SOT23-5 Package

3. Applications

- Smartphones, Tablet PC
- HDD, Storage and Solid State Memory Devices
- Portable Media Devices, Laptop & MID
- SLR Digital Cameras
- GPS and Navigation Equipment
- Industrial Handheld and Enterprise Equipment

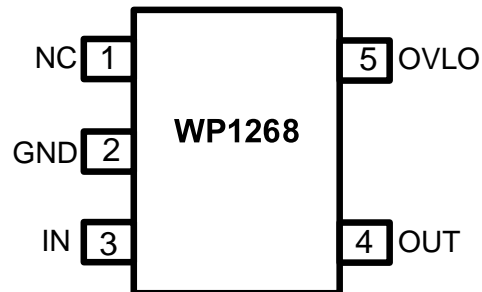
4. Typical Application



Note 1: $V_{IN_OVLO} = (1 + R1/R2) \times 1.2$, $R1 > 300k\Omega$ is recommended.

Note 2: V_{IN_OVLO} Range is 15V to 24V

5. Pin Configuration



SOT23-5

6. Pin Description

PIN NAME	PIN NUMBER	I/O	PIN FUNCTION
GND	2	-	Ground.
IN	3	I	Input.
OUT	4	O	Output.
OVLO	5	I	Adjustable OVP Threshold Input.
NC	1		No Connection

7. Absolute Maximum Ratings ^[3]

$T_A=25^{\circ}\text{C}$, unless otherwise noted.

SYMBOL	PARAMETER	RATING	UNIT
V_{IN}	IN Voltage	-0.3 to 66	V
V_{OUT}	OUT Voltage	-0.3 to 26	V
V_{OVLO}	OVLO Voltage	-0.3 to 5.5	V
I_{OUT_MAX}	Maximum Continuous Current	2	A
P_D	Power Dissipation at $T_A=+70^{\circ}\text{C}$	500	mW
$T_{J(MAX)}$	Junction Temperature	150	$^{\circ}\text{C}$
T_{STG}	Storage Temperature	-65 to 150	$^{\circ}\text{C}$
T_{SDR}	Soldering Temperature (Reflow)	260	$^{\circ}\text{C}$
V_{ESD}	Human-Body Model, Per JESD22-A114(All pins)	± 2000	V

Note 3: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

8. Recommended Operating Conditions

SYMBOL	PARAMETER	MIN	MAX	UNIT
V_{IN}	Input Voltage	4	60	V
T_A	Operating Ambient Temperature	-40	85	$^{\circ}\text{C}$
T_J	Operating Junction Temperature	-40	125	$^{\circ}\text{C}$

9. Electrical Characteristics ^[4]

($V_{IN}=12\text{ V}$, $C_{IN}=1\mu\text{F}$, $C_{OUT}=1\mu\text{F}$, $T_A=25^\circ\text{C}$, unless otherwise noted)

SYMBOL	PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Basic Operation						
V_{IN}	Input Voltage		4		60	V
I_{IN}	V_{IN} Quiescent Current	$V_{IN}=12\text{V}$, OUT Floating		120		μA
$R_{DS(ON)}$	On-Resistance of Switch IN-OUT	$V_{IN}=12\text{V}$, $I_{OUT}=1\text{A}$		110		$\text{m}\Omega$
V_{IN_OVLO}	Adjustable OVLO Threshold Range	V_{IN} Rising	15		24	V
V_{OVLO_TH}	OVLO Set Threshold		1.16	1.2	1.24	V
$V_{OVLO_HYS}^{[5]}$	Hysteresis of OVP Trip Level			15		mV
V_{UVLO_R}	Under Voltage Lockout Threshold	V_{IN} Rising		3		V
Over Temperature Protection (OTP)						
T_{SD}	Thermal Shutdown	$V_{IN}=12\text{V}$		155		$^\circ\text{C}$
ΔT_{SD}	Thermal Shutdown Hysteresis	$V_{IN}=12\text{V}$		30		$^\circ\text{C}$
Dynamic Characteristics						
t_{DEB}	Debounce Time	Time from $V_{UVLO_R} < V_{IN} < V_{OVLO}$ to $V_{OUT} = 10\%$ of V_{IN}		12		ms
t_{ON}	Switch Turn-on Time	V_{OUT} from $10\%V_{IN}$ to $90\%V_{IN}$		0.8		ms
t_{ON_ALL}	Output Power-on Time	Time from $V_{UVLO_R} < V_{IN} < V_{OVLO}$ to $V_{OUT}=90\%$ of V_{IN}		12.8		ms
$t_{OFF_RES}^{[5]}$	Switch Turn-off Response Time	$R_L=1000\Omega$, $C_L=0\mu\text{F}$, $V_{IN}>V_{OVLO}$ to V_{OUT} Stop Rising		300		ns

Note 4: Limits over full temperature are guaranteed by design, but not tested in production.

Note 5: Guaranteed by characterization testing and design.

10. Typical Performance Characteristics

($V_{IN} = 12V$, $C_{IN} = 1\mu F$, $C_{OUT} = 1\mu F$, $T_A = 25^\circ C$, unless otherwise noted)

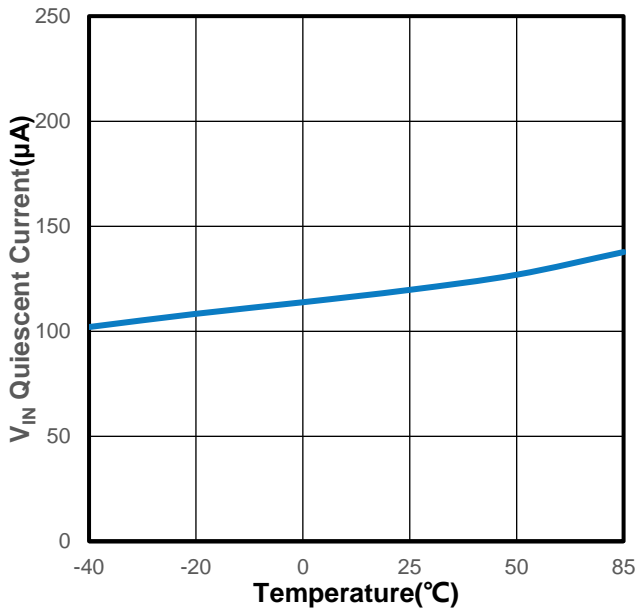


Figure 1. VIN Quiescent Current vs. Ambient Temperature

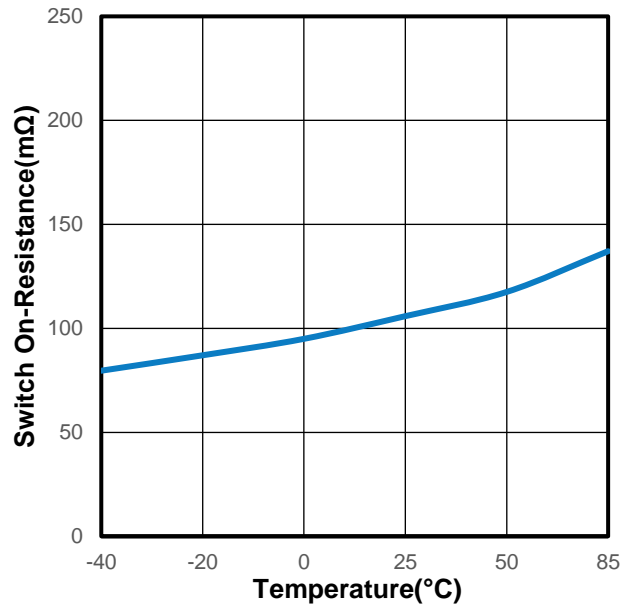


Figure 2. On-Resistance vs. Ambient Temperature

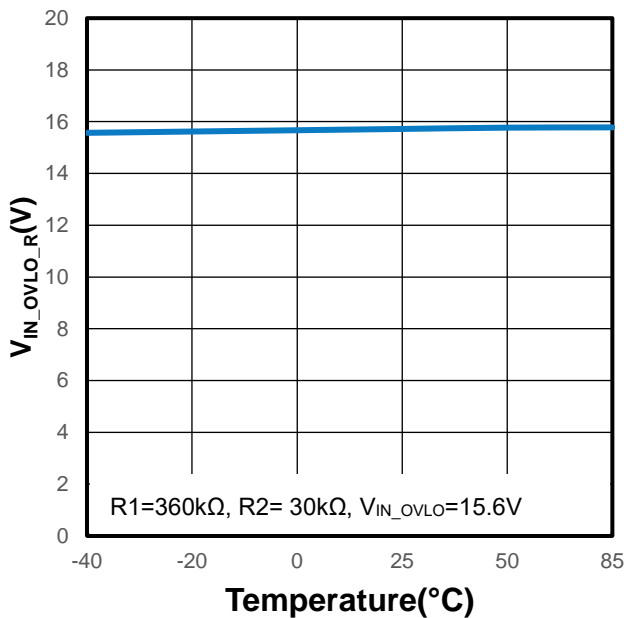


Figure 3. VIN_OVLO_R vs. Ambient Temperature

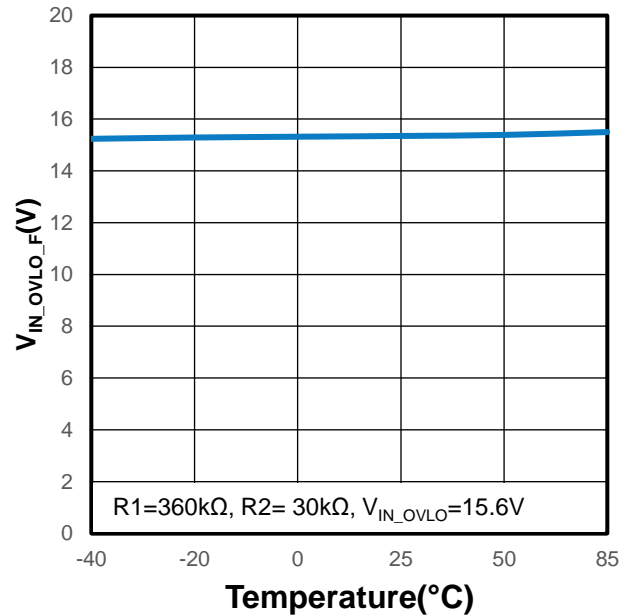
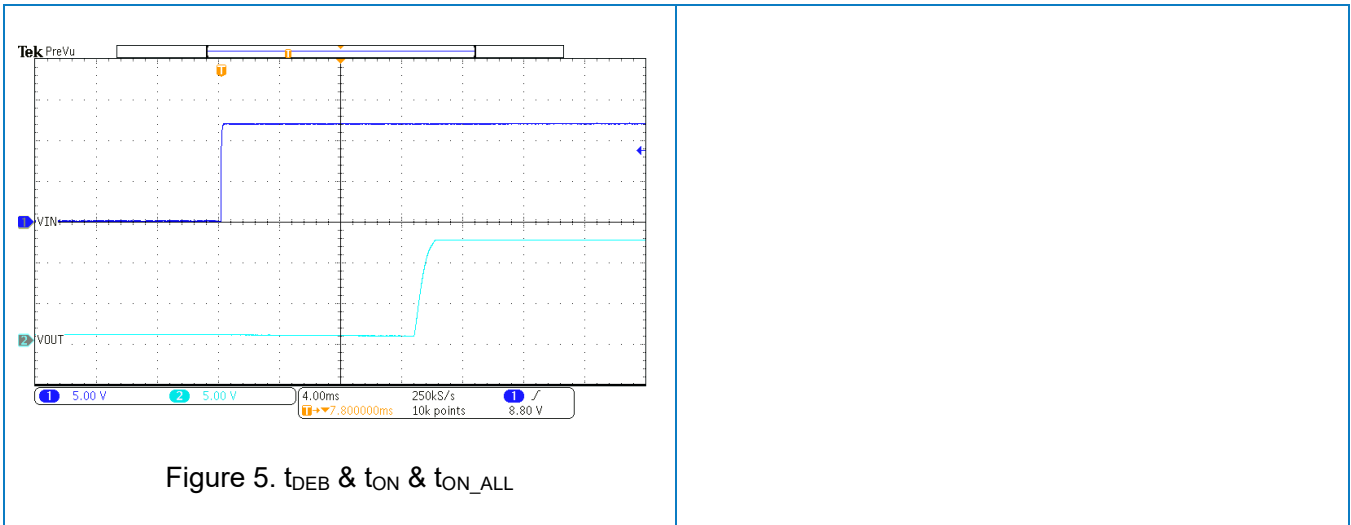


Figure 4. VIN_OVLO_F vs. Ambient Temperature



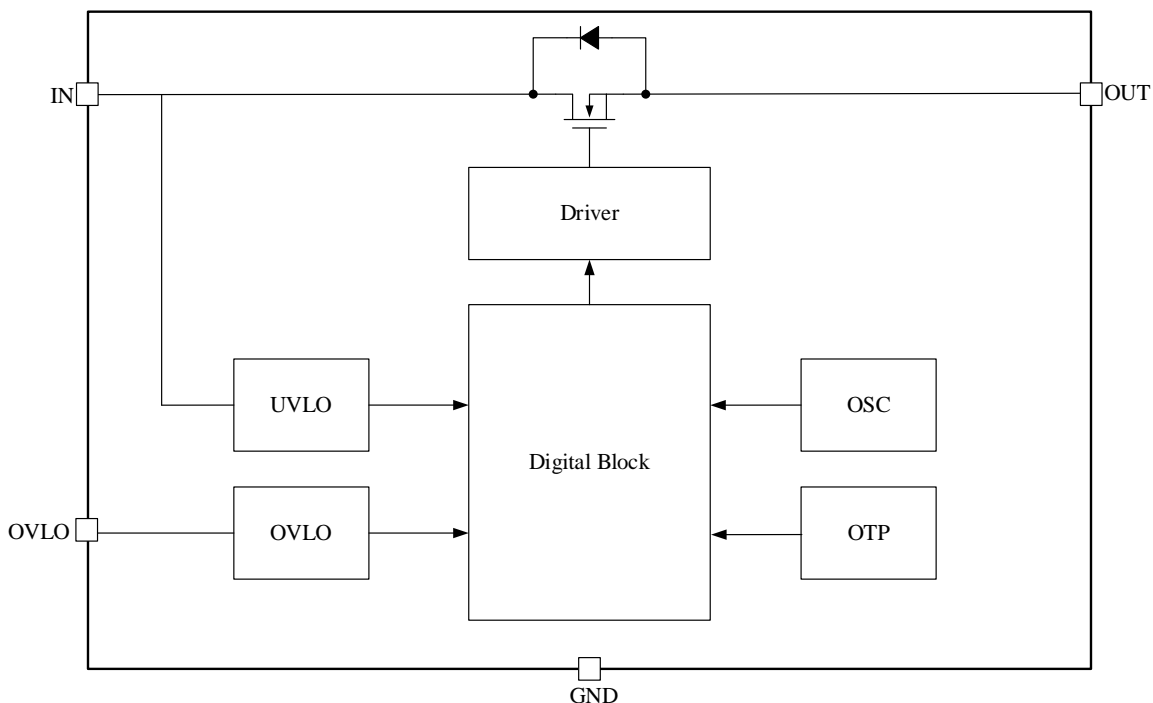
11. Function Description

11.1 Overview

The WP1268 with overvoltage protection features a low 110mΩ (Typ.) $R_{DS(ON)}$ of internal FET and protects low-voltage systems against voltage faults up to 60V_{DC}. If the V_{IN} exceeds the Overvoltage Protec Threshold, the internal FET is quickly turned off to prevent the downstream components from damage.

The internal FET turns off when the junction temperature exceeds 155°C (Typ.). The device exits thermal shutdown after the junction is cooled down by 30°C (Typ.).

11.2 Block Diagram



11.3 Feature Description

11.3.1 Under Voltage Lock Out (UVLO)

The under-voltage lockout (UVLO) circuits disable the WP1268 until the input voltage reaches the UVLO turn-on threshold.

11.3.2 Over Temperature Protection (OTP)

The WP1268 monitors its own internal temperature to prevent thermal failures. The device turns off the internal FET when the junction temperature reaches 155°C. The device will resume after the junction is cooled down by 30°C.

11.3.3 Input Over Voltage Protection (OVP)

If the input voltage exceeds the WP1268 rising trip level, the switch will be turned off in about 300ns. The switch will remain off until V_{IN} falls below the WP1268 falling trip level.

12. Application and Implementation

12.1 Selection of Input Capacitor

To limit the voltage drops on the input supply caused by transient inrush current, a capacitor must be placed between the IN and GND pins.

12.2 Selection of Output Capacitor

A capacitor should be placed between the OUT and GND pins.

13. Evaluation Modules

Evaluation Modules (EVMs) are available to help evaluate initial circuit performance. We have evaluation modules for different packages, you can contact us to get the evaluation module or schematic.

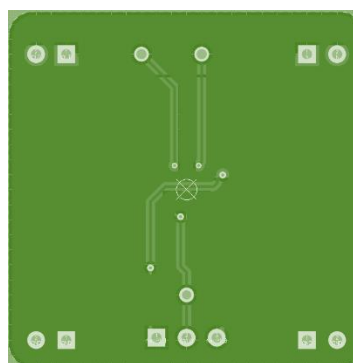
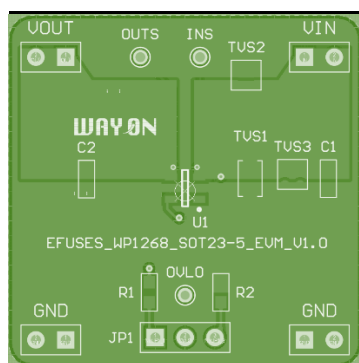
The module names are listed in the following table.

Name	Package	Evaluation Module
WP1268	SOT23-5	EFUSES_WP1268_SOT23-5_EVM_V1.0

Layout Guidelines

For best performance, all traces should be as short as possible, the input and output capacitors should be placed close to the device to minimize the effects that parasitic trace inductances may have on normal and short-circuit operation. Using wide traces for V_{IN} , V_{OUT} , and GND will help minimize parasitic electrical effects and minimize the case to ambient thermal impedance.

Layout Example



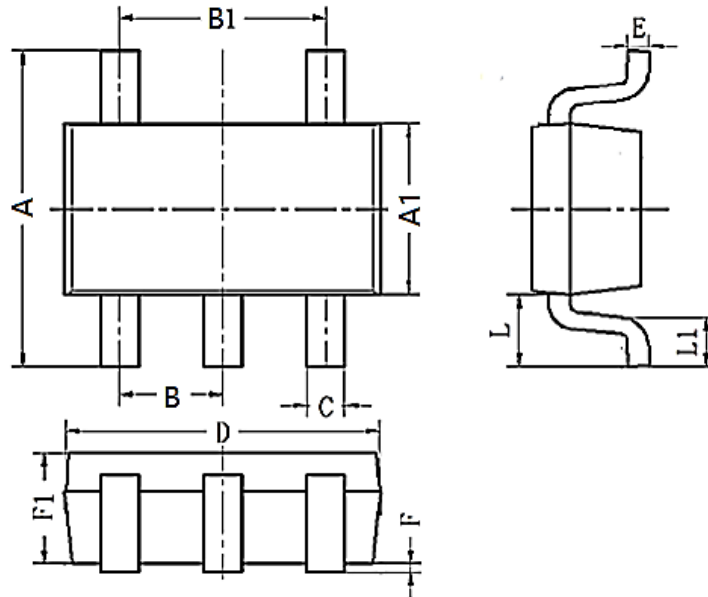
14. Naming Conventions

WP AB CC-DDD E

- WP:** WAYON Protection IC;
- A:** Product Category – 1: E-fuse;
- B:** Maximum Output Current – 2: $\leq 2A$;
- CC:** Serial number;
- DDD:** Package – A50: SOT23-5;
- E:** R-Reel & T-tube;

15. Package Information

SOT 23-5



SYMBOL	DIMENSIONS IN MILLIMETERS		
	MIN	NOM	MIN
A	2.60	2.80	3.00
A1	1.50	1.60	1.70
B	0.85	0.95	1.05
B1	1.80	1.90	2.00
C	0.25	0.37	0.50
D	2.79	2.90	3.02
E	0.10	0.15	0.20
F	0.00	0.10	0.20
L	0.60REF		
L1	0.30	0.45	0.60
F1	0.85	1.10	1.30

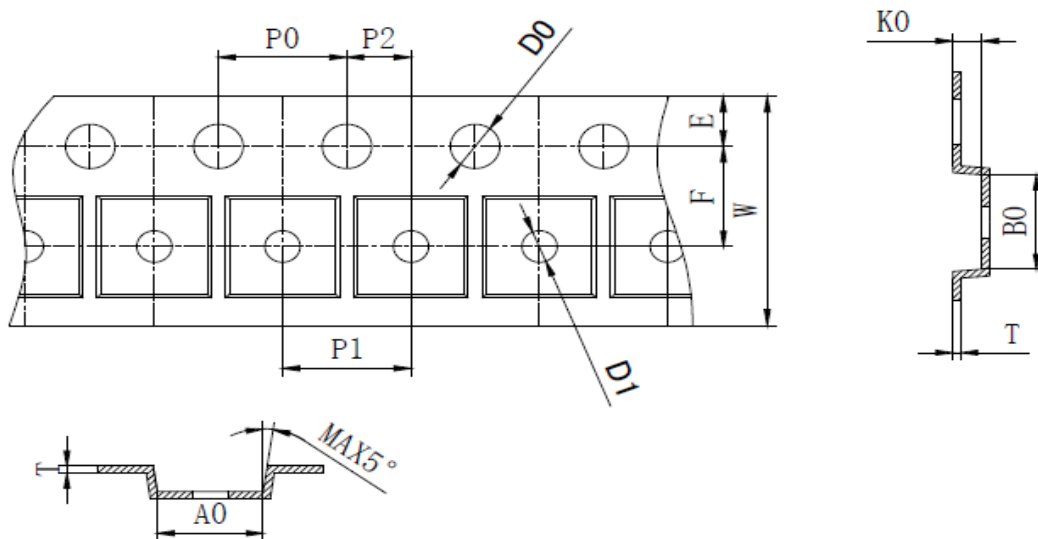
16. Ordering Information

PART NUMBER	PACKAGE	PACKING QUANTITY	MARKING*
WP1268-A50R	SOT23-5	3k/Reel	WP1268 XXXX

* XXXX is variable.

17. Package specification

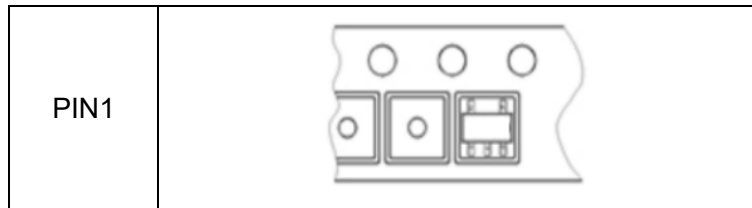
17.1 Carrier belt



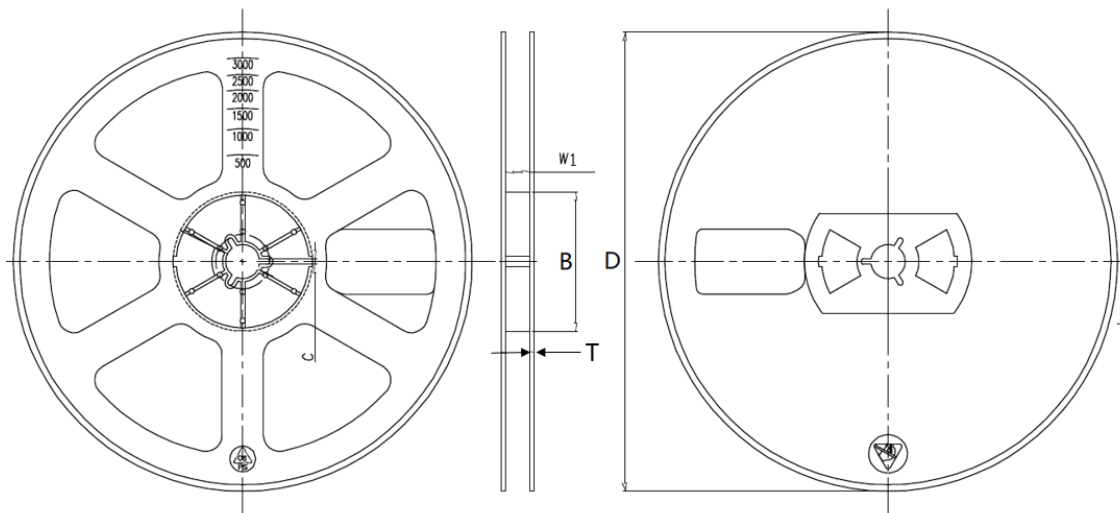
Unit: mm

A0	B0	K0	D0	D1	P0
3.08~3.40	3.05~3.50	1.22~1.60	1.50~1.60	0.95~1.25	3.90~4.10
P1	P2	W	T	E	F
3.90~4.10	1.90~2.10	7.90~8.30	0.15~0.28	1.65~1.85	3.40~3.60

17.2 Placement direction



17.3 Reels: 7inch



Unit: mm

D	W1	T	B	C
176~181	7.5~11.6	0.8~3.3	52~60.5	2~3.2

Contact Information

No.1001, Shiwan(7) Road, Pudong District, Shanghai, P.R.China.201207

Tel: 86-21-50310888 Fax: 86-21-50757680 Email: market@way-on.com

WAYON website: <http://www.way-on.com>

For additional information, please contact your local Sales Representative.

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Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.

Users should verify actual device performance in their specific applications.

Product Specification Statement

- The product specification aims to provide users with a reference regarding various product parameters, performance, and usage. It presents certain aspects of the product's performance in graphical form and is intended solely for users to select product and make product comparisons, enabling users to better understand and evaluate the characteristics and advantages of the product. It does not constitute any commitment, warranty, or guarantee.
- The product parameters described in the product specification are numerical values, characteristics, and functions obtained through actual testing or theoretical calculations of the product in an independent or ideal state. Due to the complexity of product applications and variations in test conditions and equipment, there may be slight fluctuations in parameter test values. WAYON shall not guarantee that the actual performance of the product when installed in the customer's system or equipment will be entirely consistent with the product specification, especially concerning dynamic parameters. It is recommended that users consult with professionals for product selection and system design. Users should also thoroughly validate and assess whether the actual parameters and performance when installed in their respective systems or equipment meet their requirements or expectations. Additionally, users should exercise caution in verifying product compatibility issues, and WAYON assumes no responsibility for the application of the product.
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- Users are advised to pay attention to the parameter limit values specified in the product specification and maintain a certain margin in design or application to ensure that the product does not exceed the parameter limit values defined in the product specification. This precaution should be taken to avoid exceeding one or more of the limit values, which may result in permanent irreversible damage to the product, ultimately affecting the quality and reliability of the system or equipment.
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